

TITLE: PROGRAMMING OF A FLASH MEMORY CELL INVENTOR(S): Sheung Hee Park, Wing Han Leung, and Richard M. Fastow USSN: 10/617,971 Attorney Docket #: AMD-H0523

1/10

<u>105</u>

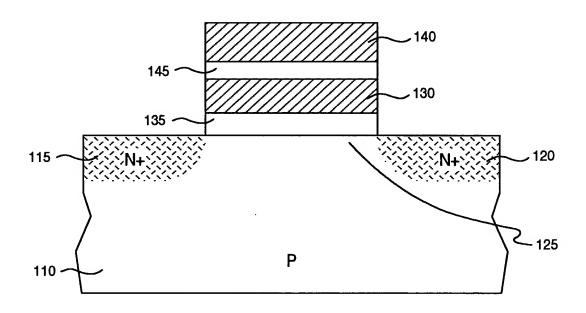


FIGURE 1A (CONVENTIONAL ART)

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USSN: 10/617,971 Attorney Docket #: AMD-H0523

2/10

BITS	
00 —	$V_{T3} = 7V$
01	V _{T2} = 5V
10	V _{T1} = 3V
11	MIN V _T = 1V

FIGURE 2A (CONVENTIONAL ART)

BITS	
000	V _{T7} = 8V
001	 $V_{T6} = 7V$
010	$V_{T5} = 6V$
011	 $V_{T4} = 5V$
100	 V _{T3} = 4V
101	 $V_{T2} = 3V$
110	V _{T1} = 2V
111	 MIN V T= 1V

FIGURE 2B (CONVENTIONAL ART)

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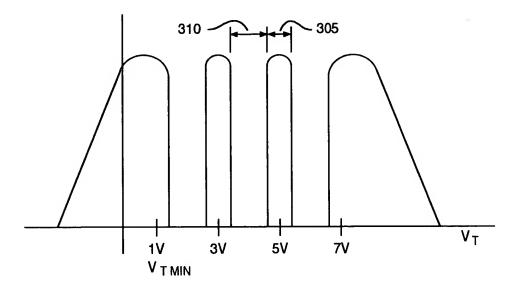
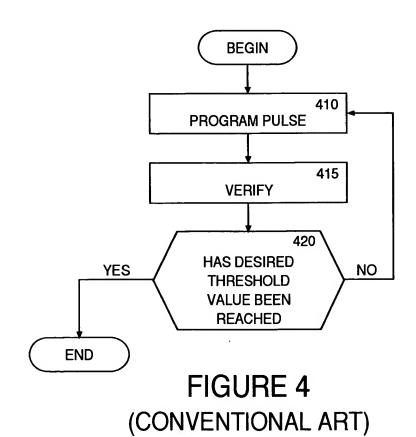


FIGURE 3 (CONVENTIONAL ART)



4/10

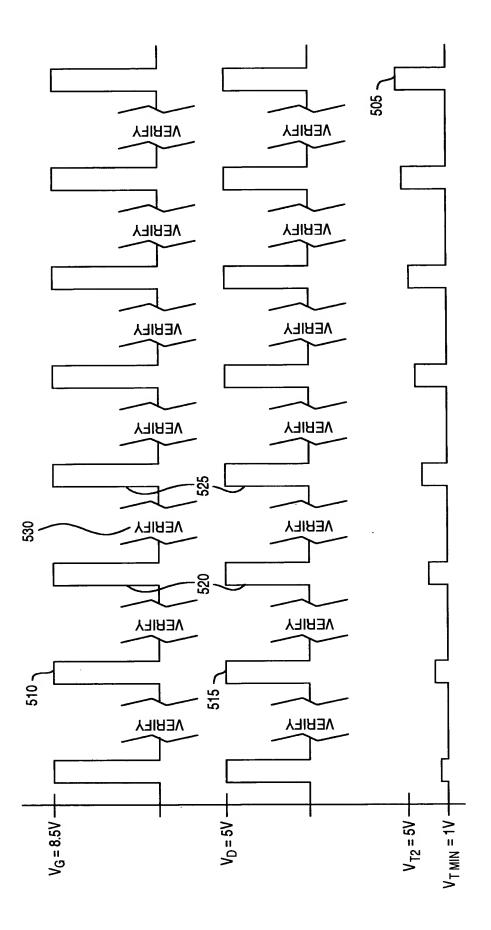


FIGURE 5 (CONVENTIONAL ART)

TITLE: PROGRAMMING OF A FLASH MEMORY CELL

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5/10

<u>605</u>

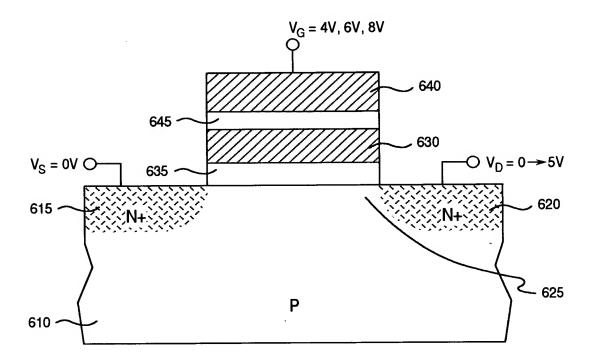


FIGURE 6

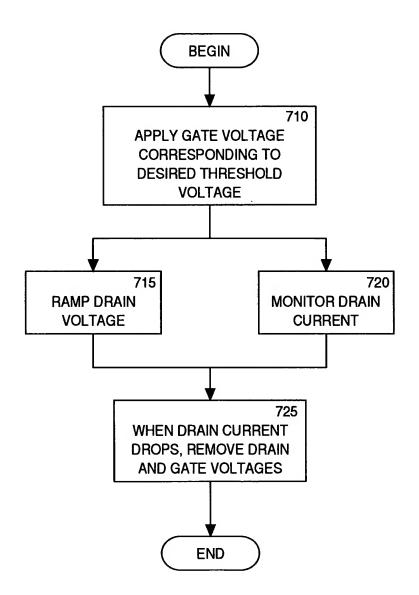


FIGURE 7

TITLE: PROGRAMMING OF A FLASH MEMORY CELL INVENTOR(S): Sheung Hee Park, Wing Han Leung, and Richard M. Fastow

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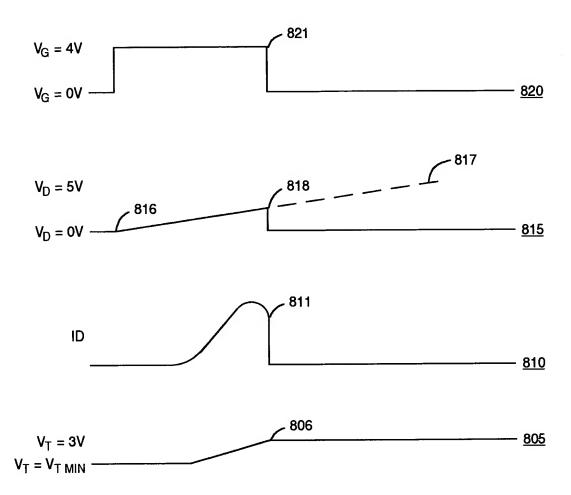


FIGURE 8A

TITLE: PROGRAMMING OF A FLASH MEMORY CELL

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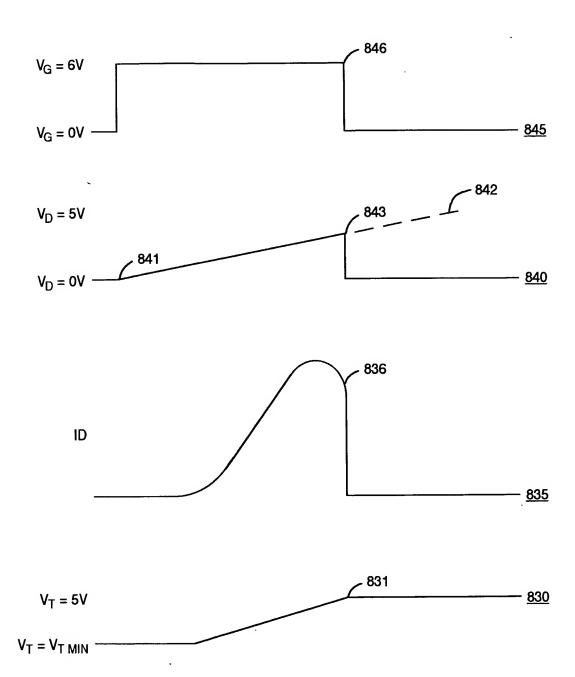


FIGURE 8B

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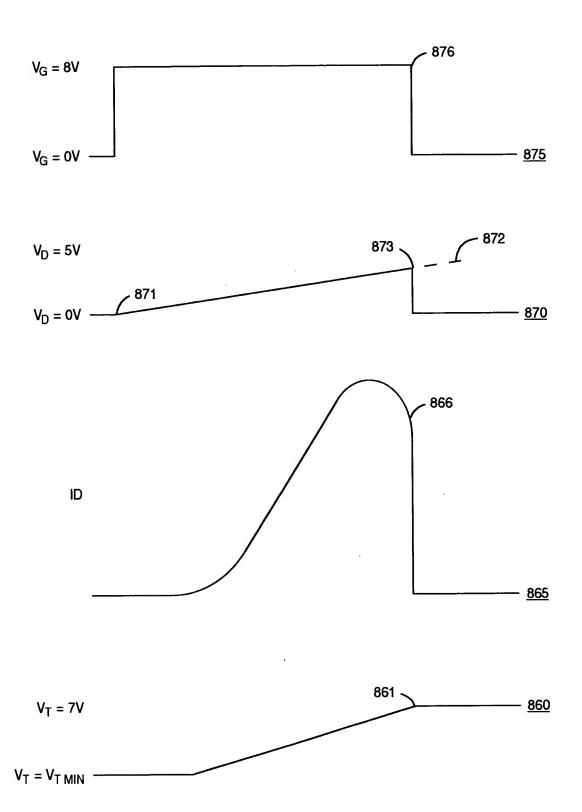


FIGURE 8C

